



- ✖ L1: (76449) sdram or (synchronous adj2 (DRAM) or (dynamic adj3 memory))
- ✖ L2: (252) write adj recovery adj time
- ✖ L3: (336) precharge adj3 ((row adj3 strobe) or RAS)
- ✖ L4: (62850) clock adj2 frequency
- ✖ L6: (1) 2 same 3
- ✖ L6: (13) 2 same 4
- ✖ L7: (1806) precharge adj2 time
- ✖ L12: (10) 2 same 7
- ✖ L14: (2198) delay\$4 with precharg\$4
- ✖ L16: (34) 2 and 14
- ✖ L18: (5) (first or second) adj2 2
- ✖ L19: (2762) (precharg\$4 or pre-charg\$4) adj2 time
- ✖ L20: (21) 2 same 19
- ✖ L24: (16348) ddr or (double adj data adj rate)
- ✖ L26: (2701) (first or second) adj2 4
- ✖ L26: (8) 24 same 26
- ✖ L27: (64) 24 and 25
- ✖ L29: (66) (precharg\$4 or pre-charg\$4) with 2
- ✖ L30: (1) 26 and 29
- ✖ L31: (9) 24 and 29
- ✖ L32: (32) 29 not (31 or 26 or 27 or 20 or 15)
- ✖ L33: (2549) 366/194
- ✖ L34: (5642) 366/233
- ✖ L36: (5037) 366/203
- ✖ L36: (84370) "366"/\$6
- ✖ L37: (29) 2 and 33
- ✖ L38: (50) 2 and 34
- ✖ L39: (53) 2 and 35
- ✖ L40: (92) 37 or 38 or 39
- ✖ L42: (31) 19 and 40
- ✖ L43: (162) 2 and 36
- ✖ L45: (12) (JACUNSKI near2 MARK).in.
- ✖ L46: (82) (NORRIS near2 ALAN).in.
- ✖ L47: (5) (WEINSTEIN near2 SAMUEL).in.

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DB: US-PGPHUB;USPAT;EPO;JPO;DERWENT;IBM_TDB

P Purse

Default operator: OR

P Highlights all hit terms initially

BRS form ISM form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Ct	Inventor	S	C	P	?	?
1			US 20030186076 A1	20031002	23	Auto-precharge control circuit	366/203			Park, Sang-Kyun et al.					
2			US 6363673 B1	20020306	26	Clock synchronization semicon	366/233	366/194		Koshikawa; Yasuji					
3			US 6434082 B1	20020813	21	Clocked memory device that in	366/233	366/203		Hovis; William Paul et al.					
4			US 6607626 B2	20030114	16	Semiconductor memory with Im	366/203	366/191		Ohtake; Hiroyuki					

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